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In re Patent Application:

Serial No. : **10/634,902**Applicant: : **SUNTER, Stephen K.**Filed : **August 6, 2003**TC / GAU : **2829**Examiner : **NGUYEN, Tung X.**Date : **July 13, 2004**Docket No. : **LVPAT061US**Customer No: **26668**

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450
United States Of America

PAPER A

Sir:

This is in response to the Official Action mailed June 30, 2004.

Remarks / Arguments begin on page 2 of this paper.



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Your File	10/634,902	Our File	LVPAT061US
Subject	SUNTER, Stephen K. CIRCUIT AND METHOD FOR ACCURATELY APPLYING A VOLTAGE TO A NODE OF AN INTEGRATED CIRCUIT United States Patent Application Serial No. 10/634,902 Filed August 6, 2003		

Message:

See attached Reply to Official Action.

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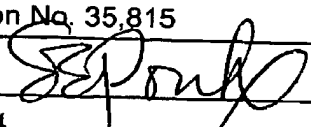
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/634,902	
	Filing Date	08/06/2003	
	First Named Inventor	SUNTER, Stephen K.	
	Group Art Unit	2829	
	Examiner Name	NGUYEN, Tung X.	
Total Number of Pages in This Submission	4	Attorney Docket Number	LVPAT061US

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